## CLAIMS

1. Processor program product (1) to be run via a processor-system for generating and/or analysing traffic signals for testing an integrated-circuit-simulation (8) running via said processor-system, which integrated-circuit-simulation (8) is designed to handle traffic signals,

characterised in that said processor program product (1) comprises at least one generic module (2,3) and at least one specific module (4,5), with at least one specific module (4,5) being designed for interfacing said computer program product (1) with a protocol used in said integrated-circuit-simulation (8).

2. Processor program product (1) according to claim 1, characterised in that said protocol comprises a traffic protocol.

3. Processor program product (1) according to claim 2, characterised in that said traffic protocol comprises an Internet-Protocol or an Asynchronous-Transfer-Mode-Protocol or an Ethernet-Protocol.

4. Processor program product (1) according to claim 1, 2 or 3, characterised in that said protocol comprises a bus protocol.

5. Processor program product (1) according to claim 4, characterised in that said bus protocol comprises a flexbus4 protocol or a SPI4.2 protocol.

6. Processor program product (1) according to any one of claims 1-5,

characterised in that at least one generic module (2,3) is designed to operate in dependence of adjustable parameters.

7. Processor program product (1) according to claim 6, characterised in that said adjustable parameters comprise a bandwidth parameter.

8. Processor program product (1) according to claim 6 or 7, characterised in that said adjustable parameters comprise a flow parameter.

9. Processor-system for running a processor program product (1) for generating and/or analysing traffic signals for testing an integrated-circuit-simulation (8) running via said processor-system, which integrated-circuit-simulation (8) is designed to handle traffic signals, characterised in that said processor program product (1) comprises at least one generic module (2,3) and at least one specific module (4,5), with at least one specific module (4,5) being designed for interfacing said computer program product (1) with a protocol used in said integrated-circuit-simulation (8).

10. Method for generating and/or analysing traffic signals for testing an integrated-circuit-simulation (8) running via a processor-system, which integrated-circuit-simulation (8) is designed to handle traffic signals, characterised in that said method comprises at least one generic step and at least one specific step, with at least one specific step being performed for interfacing with a protocol used in said integrated-circuit-simulation (8).